

EtherPod is a tiny 32-bit digital engine that brings real-time Ethernet connection.

Optimized to OSC application but it should adapt to virtually any embedded purpose which requires low latency, low jitter performance.

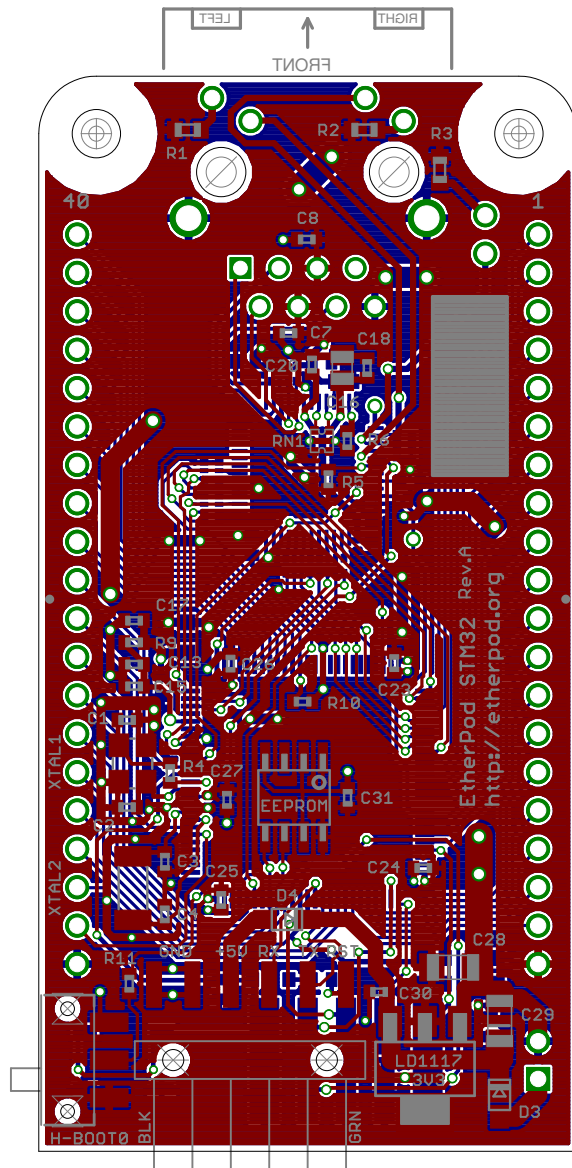
The core of EtherPod is ARM Cortex-M3 micro-controller STM32F107VC and PHY DP83848C.

*25 MHz and 32.768 KHz crystals on board.
3.3V voltage regulator on board.*

*Breadboard friendly I/O header pin-outs.
I2C, CAN, SPI, USART, USB OTG, DACs, ADCs
and digital pins (up to 14) available.
Serial and JTAG headers available.*

NuttX RTOS porting by Gregory Nutt

*TBA. USB/CAN booster
TBA. FTDI2232 JTAG booster*



EtherPod Rev.A Nov.2009

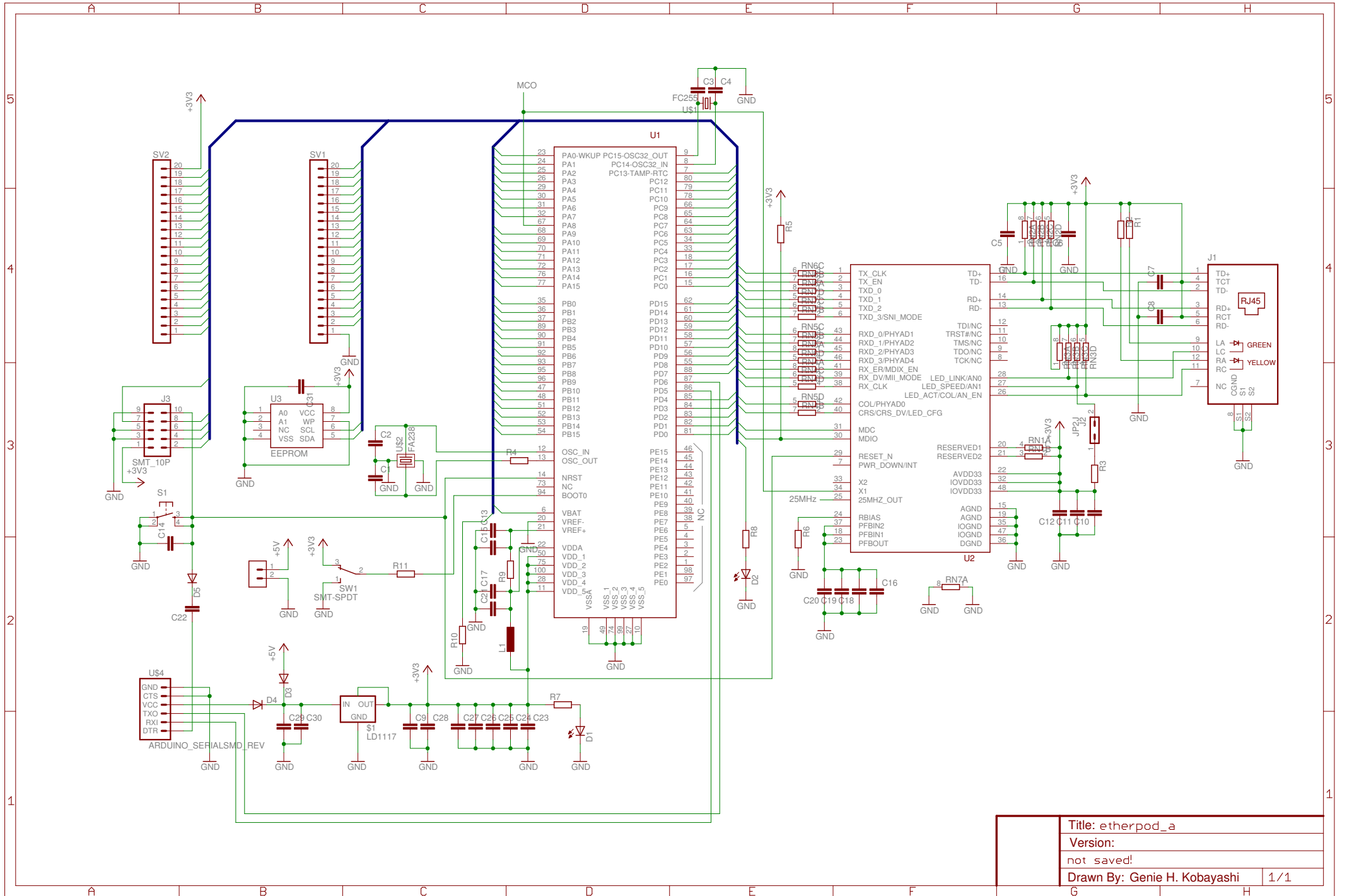
PCB size: 35.6 × 71.2 mm, 1400 × 2800 mill

Drawing scale: × 2

Designed by Genie H.Kobayashi

<http://etherpod.org>

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Drawn By: Genie H. Kobayashi	1 / 1